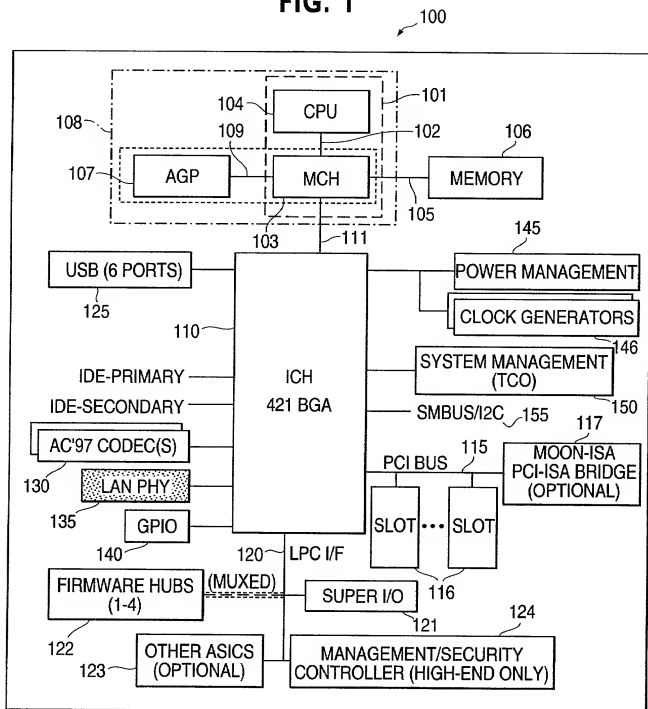
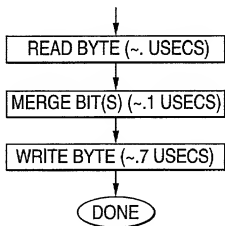


FIG. 1

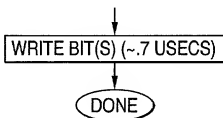




**FIG. 3**



**FIG. 5**



**FIG. 4**

	401 BIT ENABLES				402 REGISTER BITS				COMMENTS
	3	2	1	0	3	2	1	0	
INITIAL VALUE					0	0	1	1	
SOFTWARE WRITE "1010_1X0XB"	1	0	1	0	1	X	0	X	BITS 3 AND 1 ARE ENABLED TO BE OVERWRITTEN
RESULTING VALUE					1	0	0	1	BITS 3 AND 1 ARE OVER- WRITTEN, WHILE BITS 2 AND 0 RETAIN THEIR INITIAL VALUES

FIG. 6

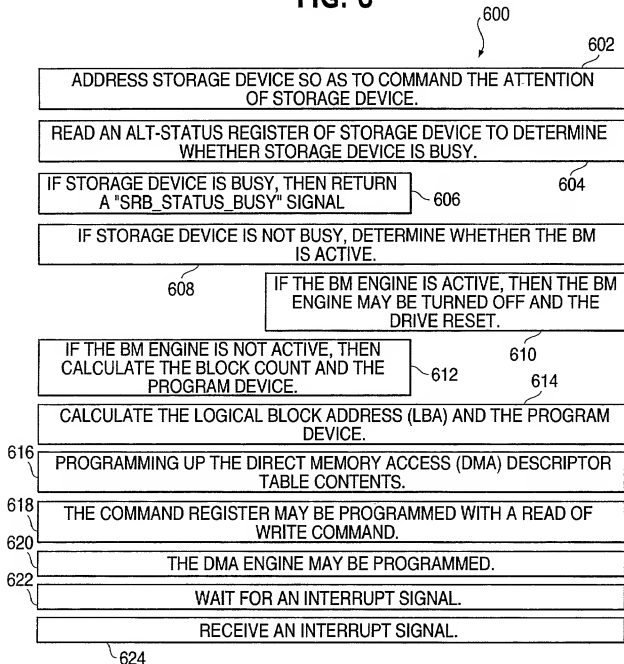


FIG. 7

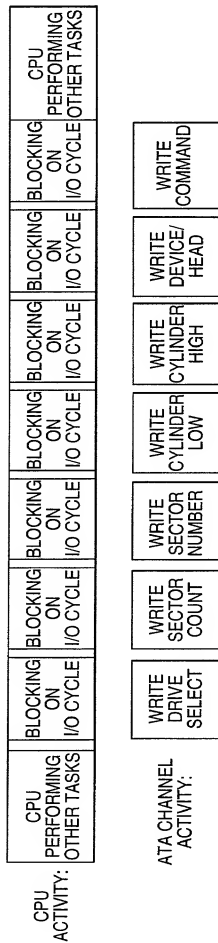


FIG. 8

